

MODEL NO. <u>BO128128CFPHH\$</u> <u>VER.04</u>



FOR MESSRS:

ON DATE OF:

APPROVED BY:

BOLYMIN, INC.

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History of Version

Version	Contents	Date	Note
01	NEW VERSION	2008/05/20	SPEC.
02	Add Handling Instruction Update Electrical Characteristics 、 Quality Assurance and Reliability Modify Backlight Information 、 Drawing	2012/12/26	Page 5 \ 8 \ 13 14 \ 16 \ 17
03	Modify Handling Instruction	2013/02/06	Page 6
04	Modify Handling Precaution Absolute Maximum Rating Electrical Characteristics Quality Assurance and Reliability	2016/05/23	Page 5~10 15~17

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- 4. Absolute Maximum Rating
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 - 12-2.3 Initializing by Instruction

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BOLYMIN 1. Numbering System <u>B</u> <u>128128</u> <u>C</u> <u>0</u> <u>E</u> <u>P</u> Н <u>H</u> <u>\$</u> _ 9 0 1 4 5 6 7 8 2 3

0	Brand	Bolymin	
1	Module Type	C= character type G= graphic type P= TAB/TCP type	O= COG type F= COF type L=PLED/OLED
2	Format	2002=20 characters, 2 lines 12232= 122 x 32 dots	
3	Version No.	A type	
4	LCD Color	G=STN/gray Y=STN/yellow-green PLED/yellow-green C=color STN,OLED/RGB	B=STN/blue,OLED/blue F=FSTN T=TN
5	LCD Type	R=positive/reflective P=positive/transflective	M=positive/transmissive N=negative/transmissive
6	Backlight type/color	L=LED array/ yellow-green H=LED edge/white R=LED array/red G=LED edge/yellow-green F=RGB array I=RGB edge Q=LED edge/red N=No backlight	D=LED edge/blue E=EL/white B=EL/blue C=CCFL/white Y=LED Bottom/yellow O=LED array/orange K=LED edge/green A=LED edge/amber
7	CGRAM Font (applied only on character type)	J=English/Japanese Font E=English/European Font G=Chinese(simple) F=Chinese(traditional)	C=English/Cyrillic Font H=English/Hebrew Font A=English/Arabic Font
8	View Angle/ Operating Temperature	B=Bottom/Normal Temperature H=Bottom/Wide Temperature U=Bottom/Ultra wide Temperature	T=Top/Normal Temperature W=Top/Wide Temperature C=9H/Normal Temperature E=Top/ultra wide temperature
9	Special Code	3=3.3 volt logic power supply n=negative voltage for LCD c=cable/connector xxx=to be assigned on datasheet	t=temperature compensation for LCD p=touch panel \$=RoHS

2. Handling Precaution

2.1 Precaution in use of LCD Module

- 2.1.1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure and/or sharp tools on the surface of display area.
- 2.1.2. The polarizer placed on the display surface is easily scratched and damaged. Extreme care should be taken when handling it. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isopropyl alcohol, ethyl alcohol, do not use water, ketone or aromatics to clear display surface, and never scrub it hard.

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- 2.1.3. Keep LCD panels away from direct sunlight. The storage environment should be dust-free, clean, dry, temperature is 25°C±10°C and the humidity is below 55% RH.
- 2.1.4. Do not input any signal before power is turned on.
- 2.1.5. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- 2.1.6. It's important to control soldering temperature and time. RoHS compliant materials might need higher temperature and time, but try to keep temperature under 350°C and time in 3-5 sec.
- 2.1.7. EL is manufactured from the organic film, and is easily affected by temperature, humidity and other environmental impact. Long time storage might cause low quality of the case. Therefore, please start production in 3 months after reception of the LCM. If in any case, long time storage over 3 months is necessary, please keep EL in vacuum package or at least in humidity < 35% RH, and temperature 25°C±10°C. Note: 2.1.7. is applied to EL backlight only.</p>

2.2 Static Electricity Precautions:

- 2.2.1. The LCD module contains a C-MOS LSI. People who operate the LCM should wear ESD protection equipment to prevent ESD hurt on products.
- 2.2.2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- 2.2.3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
- 2.2.4. The modules should be kept in anti-static bags or trays for storage.
- 2.2.5. Only properly grounded soldering irons should be used.
- 2.2.6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 2.2.7. The normal static prevention measures should be observed for work clothes and working benches.
- 2.2.8. Since dry air(almost low RH) is inductive to static, a humidity of 50-60% RH is recommended in assembly line.

2.3 Operation Precautions:

- 2.3.1. DC voltage applied on LCM causes electrochemical reactions, which will deteriorate the display over time. The applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 2.3.2. LCD driving voltage should be kept within specified range; excess voltage will shorten display life, while less voltage may not turn on LCM.
- 2.3.3. LCM response time will be extremely delayed in low operating temperature(such as -20 °C) than in room operating temperature. Therefore, higher LCD driving voltage is required in low operating temperature; On the other hand, in high operating temperature (such as +70°C) LCD shows dark background color, therefore lower LCD driving voltage is required. Be sure to use the specified LCD driving voltage in different operating temperature.

2.4 Safety:

2.4.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin. If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

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2.5 WARRANTY POLICY

Bolymin .Will provide one-year warranty for the products only if under specification operating conditions.

If there are functional defects found during the period of warranty, the defective products would be replaced on a one-to-one basis.

Bolymin would not be responsible for any direct/indirect liabilities consequential to any parties.

2.6 MTBF

- 2.6.1 .By specific test condition, MTBF based on 30°C normal operation temperature is 50,000hours.
- 2.6.2 Test Condition:

2.6.2.1 Supply Voltage for LCM: Typical Vdd

- 2.6.2.2 CC (Constant Current) mode and typical current is applied for LED.
- 2.6.2.3 Run-Patterns: by Bolymin's test program that has defined patterns and cyclic period.
- 2.6.2.4 Humidity: 60%RH

2.6.3 Test Criteria:

Attenuation of average brightness: \leq 50%

Increasing of current consumption for LCM/Backlight: \leq 20%

Display function at room temperature: Normal

Appearance: Normal



3. General Specification

(1) Mechanical Dimension

Item	Dimension	Unit
Number of Dots	128 x 128	dots
Module dimension (L x W x H)	38.5 x 95.7 x 4(MAX)	mm
View area	30.5 x 32.0	mm
Active area	27.25 x 29. 81	mm
Dot size	0.199x 0.219	mm
Dot pitch	0.213 x 0.233	mm
(2) Controller IC: ST7528 contr	oller	

4. Absolute Maximum Ratings

4.1 Electrical Absolute Maximum Ratings

(Vss=0V, Ta=25°C)

				(100 (, iu 200)
Item	Symbol	Min	Тур	Max	Unit
Supply Voltage For Logic	Vdd-Vss	1.8	-	3.3	V
Supply Voltage For LCD	Vo-Vss	-	+11	-	V

4.2 Environmental Absolute Maximum Ratings

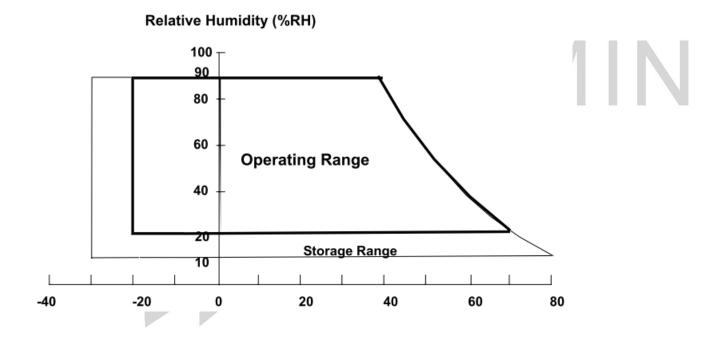
Item	Symbol	Min	Max	Unit	Note
Operating Temperature	ТОР	-20	70	°C	(1)
Storage Temperature	TST	-30	80	°C	(1)

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Note (1)

- (a) 90 %RH Max. (Ta <= 40 °C).
- (b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).

(c) No condensation.





5. Electrical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	Vdd-Vss		3.0	3.3	3.6	V
Supply Voltage For LCD	Vo-Vss	Ta=25°C	13.4	13.7	14.0	V
Input High Volt.	VIH		1.8		Vdd	V
Input Low Volt.	VIL		0		0.8	V
Output High Volt.	VOH		0.7*Vdd		Vdd	V
Output Low Volt.	VOL		Vss		0.3*Vss	V
Supply Current	Idd	Vdd=3.3V		0.5	—	mA
LCM Surface Luminance Ta=25°C	L	I _{LED} =30mA Display all OFF	35	52	_	cd/m ²

*Optimum LCD driving voltage value, referring to above mentioned range, is changed due to

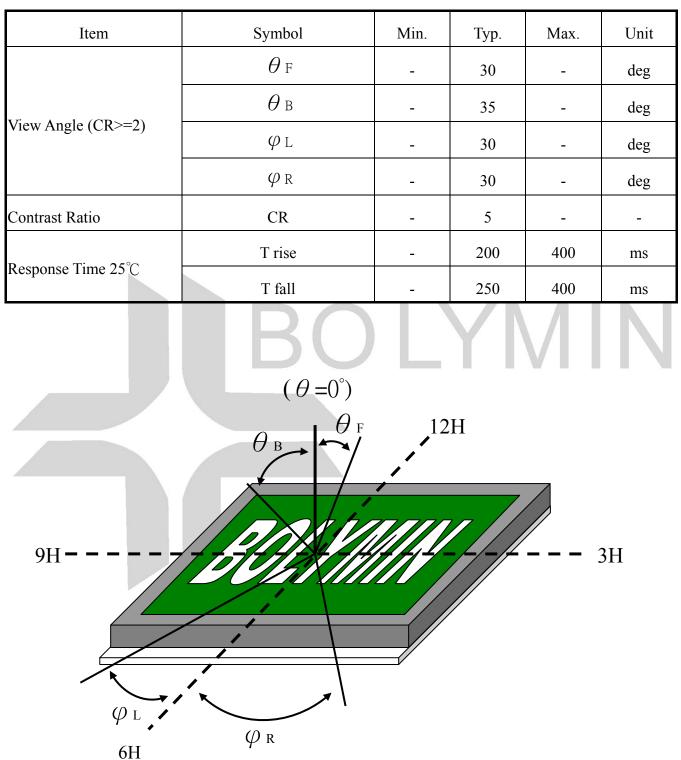
different batch of LCD glass.

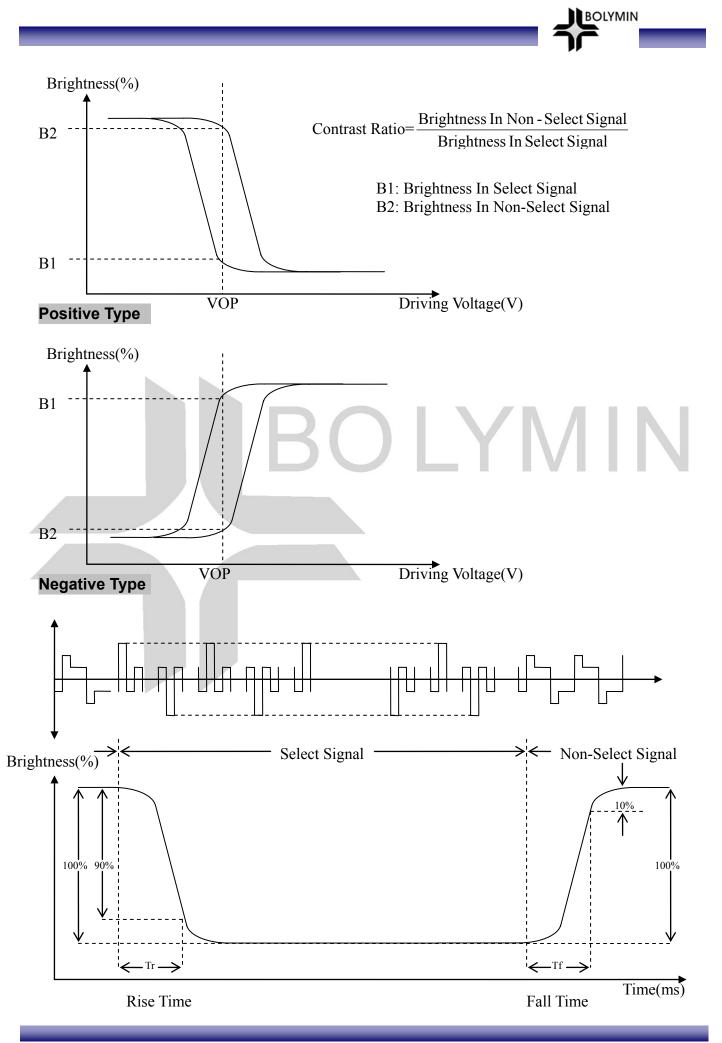


6.Optical Characteristics

a. FSTN

(Ta=25℃)







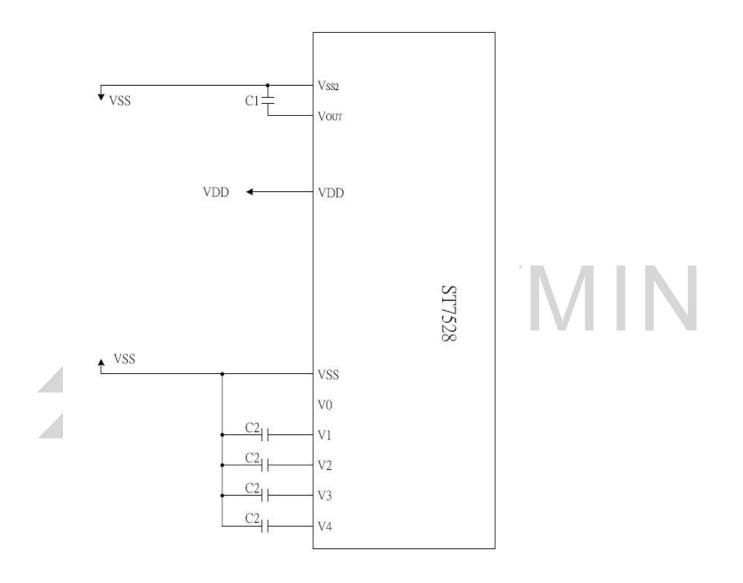
7.Interface Pin Function

Pin No.	Symbol	Level	Description					
1	NC	-	No connection					
2	V0	-						
3	V1	-						
4	V2	-	LCD driver voltage $V0 \ge V1 \ge V2 \ge V3 \ge V4 \ge VSS$					
5	V3	-	$v_0 \leq v_1 \leq v_2 \leq v_3 \leq v_4 \leq v_{33}$					
6	V4	-						
7	VOUT	-	LCD driver voltage					
8	VSS	-	Ground					
9	VDD	-	Power supply (+3.3V)					
10	SID	H/L	Serial Data Input					
11	SCLK	H/L	Serial Clock Input					
12	A0	H/L	H=Data L=Instruction					
13	/RESET	L	Reset : L=Enable H=Disable					
14	/CSB	L	L=chip selected H=chip unselected					

8. Power supply for LCD Module

* Bias Power Save circuit

When we set the Instruction of Bias Power Save, the bias also could be working, and the IC current consumption will be lower about 100uA to 200uA



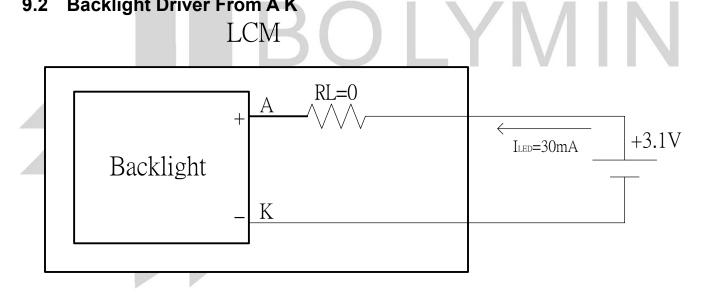
C1= 1u F ~ 4.7u F, C2 = 0.1u F ~ 1u F (suggestion value: C1=1uF, C2=0.1uF)



9. Backlight information 9.1. edge LED/white

Parameter	Symbol	Min	Тур	Max	Unit	Test Condition
Supply Current	ILED		30		mA	V=3.1V
Supply Voltage	V	2.9	3.1	3.3	V	ILED=30mA
Reverse Voltage	VR			5	V	
	X	0.27		0.32		
CIE	Y	0.28	_	0.33		ILED=30mA
Color				White		

Backlight Driver From A K 9.2



10. Quality Assurance

10.1 Inspection conditions

- 1. The LCD shall be inspected under 20~40W white fluorescent light.
- 2. Checking Direction shall be in the 40 degree from perpendicular line of specimen surface.

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- 3. Checker shall see over 30 cm.
- 4. Inspect about 5 seconds for each side.
- 5. Defect that is located at outside of VA and doesn't affect function is ignored.

10.2 Inspection Parameters

NO.	Parameter	Criteria								
		Zone Dimension D≤0.10	Acceptable Number Disregard	Class Of Defects	Acceptable Level					
1	Black or White spots (Particle)	$\begin{array}{c} 0.10 \! < \! D \! \leq \! 0.2 \\ \hline 0.2 \! < \! D \! \leq \! 0.3 \end{array}$	4	Minor	2.5	IN				
		D=(Long + Shor Total defects sho	0.3 < D							
		3 1 1								
		Zone X(mm) Y(mn	Acceptab n) Number			,				
		- 0.05≧	-	d						
2	Scratch,	4.0≧L 0.05≧		— Minor	2.5					
2	Substances	3.0≧L 0.1≧ ¹ − 0.1<	-							
		X: Length Y: N Total defects sho Defect that is loo ignored.			doesn't affect fu	⊔ unction is				

3	Air Bubbles (between glass & polarizer)	Zone DimensionAcceptable NumberClass Of DefectsAcceptable Level $D \leq 0.2$ Disregard 0.2 < D ≤ 0.5 3Minor2.5 $0.5 < D$ 002.5Total defects shall not excess 3/module.Defect that is located at outside of AA and doesn't affect function is ignored.Bobble is sawn only under reflection light is disregarded.
4	Displaying Pattern	1. Incomplete or broken line is not allowed. 2. Pinholes Dimension $\Phi(mm)$ Criteria Class Of Acceptable $\Phi < 0.1$ Disregard $0.1 < \Phi \le 0.2$ 2 $0.2 < \Phi \le 0.25$ 1 $0.25 < \Phi$ 0 $1 < \Phi \le 0.25$ 1 $0.25 < \Phi$ 0 0 = (x+y)/2 3. Deformation Dimension $\Phi(mm)$ Criteria Class Of Acceptable Level $\Phi < 0.15$ Disregard $\Phi \le 0.25$ and $X \le 1/2D$ 3 $\Phi > 0.25$ and $X \le 1/2D$ 3 $\Phi > 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.15$ Disregard $\Phi \le 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.15$ Disregard $\Phi < 0.15$ Disregard $\Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge 1/2D$ 0 $1 < \Phi < 0.25$ and $X \ge $
Other	Increation sta	ndard reference Bolymin standard.

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Other Inspection standard reference Bolymin standard.

11.Reliability

■Content of Reliability Test

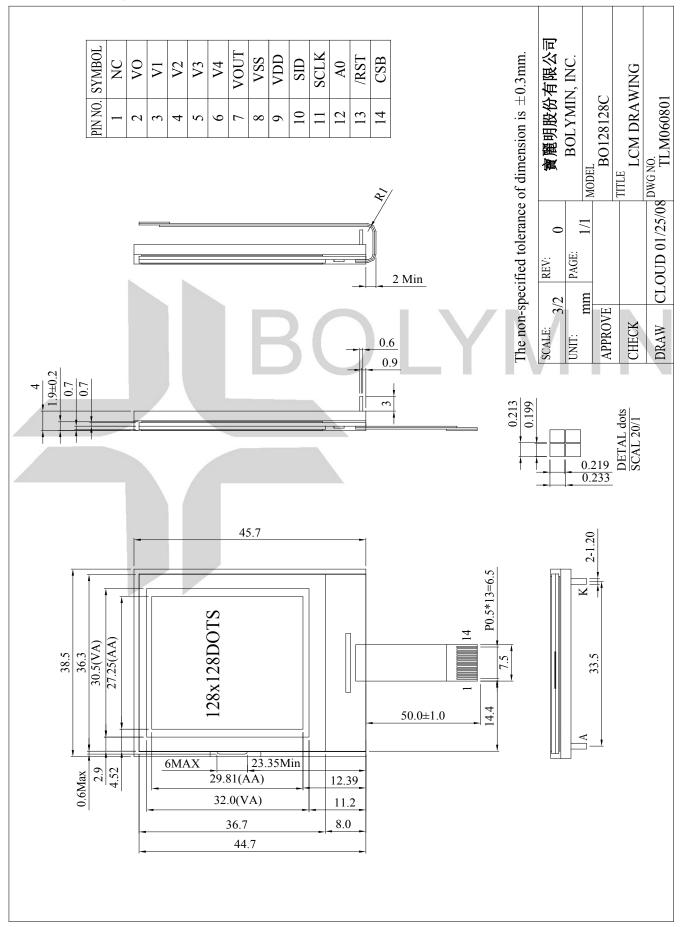
Envi	ronmental Test			
No	Test Item	Content of Test	Test Condition	Applicable Standard
1	High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 96 hrs	
2	Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30℃ 96 hrs	
3	High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 96 hrs	
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 96 hrs	
5	Humidity Test	Endurance test applying the high humidity storage for a long time.	40°C,90%RH 96hrs	
		Endurance test applying the low and high temperature cycle.		
6	Temperature cycle (Non-operation)	-30°C 80°C ◀ → → 30min 30min 1 cycle	-30℃/80℃ 10 cycles	
7	Vibration test	Endurance test applying the vibration during transportation and using.	Total Fixed Amplitude:1.5mm Vibration Frequency :10~55Hz One cycle 60 seconds to 3 direction of X,Y,Z for each 15minutes	

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%Assess after placing at normal temperature and humidity for 4 hour \circ No abnormalities in functions and appearance \circ



12.Appendix (Drawing , ST7528 controller data) 12.1 Drawing





12.2 ST7528 controller data

12.2.1. Instruction table

Instruction	A0	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description
EXT=0 or 1	EXT=0 or 1										
Mode Set	0	0	0	0	1	1	1	0	0	0	2-byte instruction to set Mode and
Mode Set	0	0	FR3	FR2	FR1	FR0	0	BE	x'	EXT	FR(Frame frequency control) BE(Booster efficiency control)
EXT=0	<u></u>								,		
Read display data	1	1				Read	data				Read data into DDRAM
Write display data	1	0				Write	data				Write data into DDRAM
Read status	0	1	BUSY	ON	RES	MF2	MF1	MF0	DS1	DS0	Read the internal status
ICON control register ON/OFF	0	0	1	0	1	0	0	0	1	ICON	ICON=0: ICON disable(default) ICON=1: ICON enable & set the page address to 16
Set page address	0	0	1	0	1	1	P3	P2	P1	P0	Set page address
Set column address MSB	0	0	0	0	0	1	Y9	Y8	Y7	Y6	Set column address MSB
Set column address LSB	0	0	0	0	0	0	Y5	Y4	Y3	Y2	Set column address LSB
Set modify-read	0	0	1	1	1	0	0	0	0	0	Set modify-read mode
Reset modify-read	0	0	1	1	1	0	1	1	1	0	release modify-read mode
Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=0: Display OFF D=1: Display ON
Set initial display line register	0	0	0	1	0	0	0	0	X'	x'	2-byte instruction to specify the initial display line to realize
	0	0	X'	S6	S5	S4	S3	S2	S1	S0	vertical scrolling
Set initial COM0 register	0	0	0	1	0	0	0	1	X'	x'	2-byte instruction to specify the initial COM0 to realize
	0	0	X	C6	C5	C4	C3	C2	C1	C0	window scrolling
Select partial display line	0	0	0	1	0	0	1	0	X'	x'	2-byte instruction to set partial
Geneer partial display inte	0	0	D7	D6	D5	D4	D3	D2	D1	D0	display duty ratio
	0	0	0	1	0	0	1	1	X'	X'	2-byte instruction to set N-line
Set N-line inversion	0	0	x'	x'	X	N4	N3	N2	N1	NO	inversion register
Release N-line inversion	0	0	1	1	1	0	0	1	0	0	Release N-line inversion mode
Reverse display ON/OFF	0	0	1	0	1	0	0	1	1	REV	REV=0: normal display REV=1: reverse display
Entire display ON/OFF	0	0	1	0	1	0	0	1	0	EON	EON=0: normal display EON=1: entire display ON

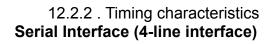


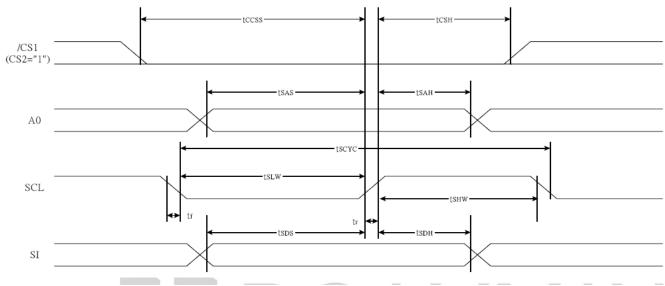
Instruction	A0	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description		
Ext=0			10			10							
Power control	0	0	0	0	1	0	1	VC	VR	VF	Control power circuit operation		
Select DC-DC step-up	0	0	0	1	1	0	o	1	DC1	DC0	Select the step-up of internal voltage converter		
Select regulator register	0	0	0	0	1	0	0	R2	R1	R0	Select the internal resistance ratio of the regulator resistor		
Select electronic volumn	0	0	1	0	0	0	0	0	0	1	2-byte instruction to specify		
register	0	0	x'	X	EV5	EV4	EV3	EV2	EV1	EV0	the reference voltage		
Select LCD bias	0	0	0	1	0	1	0	B2	B1	В0	Select LCD bias		
Set Dies Dewer Seve Mede	0	0	1	1	1	1	0	0	1	1	Bias Power save		
Set Bias Power Save Mode	0	0	0	0	0	0	0	0	0	0	Save the Bias current consumption		
Release Bias Power Save	0	0	1	1	1	1	0	0	1	1	Bias Power save release		
Mode	0	0	0	0	0	0	0	1	0	0	set the Bias power to normal		
SHL select	0	0	1	1	0	0	SHL	x'	x'	X'	COM bi-directional selection SHL=0: normal direction SHL=1: reverse direction		
ADC select	0	0	1	0	1	0	0	0	0	ADC	SEG bi-direction selection ADC=0: normal direction ADC=1: reverse direction		
Oscillator on start	0	0	1	0	1	0	1	0	1	1	Start the built-in oscillator		
Set power save mode	0	0	1	0	1	0	1	0	0	Р	P=0: normal mode P=1: sleep mode		
Release power sa∨e mode	0	0	1	1	1	0	0	0	0	1	release power save mode		
Reset	0	0	1	1	1	0	0	0	1	0	initial the internal function		
Set data direction &	X,	X,	1	1	1	0	1	0	0	0	2-byte instruction to specify		
display data length(DDL)	X,	X,	D7	D6	D5	D4	D3	D2	D1	D0	the number of data bytes. (SPI mode)		
Select FRC and PWM mode	0	0	1	0	0	1	0	FRC	PWM1	PWM0	FRC(1:3FRC, 0:4FRC) PWM1 PWM0 0 0 45PWM 0 1 45 PWM 1 0 60PWM 1 1		
NOP	0	0	1	1	1	0	0	0	1	1	No operation		
Test Instruction	0	0	1	1	1	1	x'	x'	x'	x'	Don't use this instruction		



Instruction	A0	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description		
EXT=1													
Set white mode and 1 st frame,	0	0	1	0	0	0	0	0	0	0	-Set white mode and 1st frame		
set pulse width	0	0	X'	X'	GA05	GA04	GA03	GA02	GA01	GA00	Set white mode and 1st hame		
Set white mode and 2 nd frame,	0	0	1	0	0	0	0	0	0	1	Set white mode and 2nd		
set pulse width	0	0	X'	X'	GA05	GA04	GA03	GA02	GA01	GA00	frame		
Set white mode and 3 rd frame,	0	0	1	0	0	0	0	0	1	0	Set white mode and 3rd		
set pulse width	0	0	X'	X	GA05	GA04	GA03	GA02	GA01	GA00	frame		
Set white mode and 4 th frame,	0	0	1	0	0	0	0	0	1	1	Set white mode and 4th		
set pulse width	0	0	X	X'	GA05	GA04	GA03	GA02	GA01	GA00	frame		
Set gray level 1 mode	0	0			84	4H~871	- H (4 b	ytes)	211 211		Set gray level1		
Set gray level 2 mode	0	0			88	3H~8BI	H (4 b	ytes)			Set gray level2		
Set gray level 3 mode	0	0	8CH~8FH (4bytes)								Set gray level3		
Set gray level 4 mode	0	0	90H~93H (4bytes)							Set gray level4			
Set gray level 5 mode	0	0	94H~97H (4bytes)								Set gray level5		
Set gray level 6 mode	0	0	98H~9BH (4 bytes)							Set gray level6			
Set gray level 7 mode	0	0	9CH~9FH (4 bytes)							Set gray level7			
Set gray level 8 mode	0	0	A0H~A3H (4 bytes)							Set gray level8			
Set gray level 9 mode	0	0	A4H~A7H (4 bytes)							Set gray level9			
Set gray level 10 mode	0	0	A8H~ABH (4 bytes)							Set gray level10			
Set gray level 11mode	0	0	ACH~AFH (4 bytes)						Set gray level11				
Set gray level 12 mode	0	0	B0H~B3H (4 bytes)							Set gray level12			
Set gray level 13 mode	0	0	B4H~B7H (4 bytes)							Set gray level13			
Set gray level 14 mode	0	0	B8H~BBH (4 bytes)							Set gray level14			
Set Dark mode and 1st frame,	0	0	1	0	1	1	1	1	0	0	Set Dark mode and 1st		
set pulse width	0	0	X	X	GAF5	GAF4	GAF3	GAF2	GAF1	GAF0	frame, set pulse width		
Set Dark mode and 2nd frame,	0	0	1	0	1	1	1	1	0	1	Set Dark mode and 2nd		
set pulse width	0	0	X	X	GAF5	GAF4	GAF3	GAF2	GAF1	GAF0	frame, set pulse width		
Set Dark mode and 3rd frame,	0	0	1	0	1	1	1	1	1	0	Set Dark mode and 3rd		
set pulse width	0	0	X	X	GAF5	GAF4	GAF3	GAF2	GAF1	GAF0	frame, set pulse width		
Set Dark mode and 4th frame,	0	0	1	0	1	1	1	1	1	1	Set Dark mode and 4th		
set pulse width	0	0	X	X	GAF5	GAF4	GAF3	GAF2	GAF1	GAF0	frame, set pulse width		



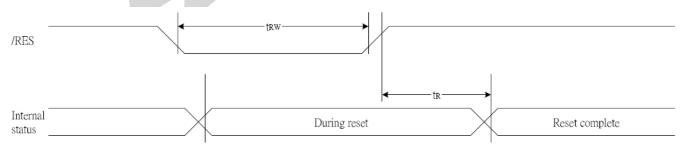




(V_{DD}=3.3V,Ta=-30~85℃)

		Symbol		Rat		
Item	Signal		Condition	Min.	Max.	Units
Serial Clock Period		tSCYC		50	_	
SCL "H" pulse width	SCL	tSHW		25	_	
SCL "L" pulse width		tSLW		25	_	
Address setup time		tSAS		20	_	
Address hold time	A0	tSAH		10	_	ns
Data setup time	01	tSDS		20	_	
Data hold time	SI	tSDH		10	_	
CS-SCL time	000	tCSS		20	_	1
CS-SCL time	CSB	tCSH		40	_	1

Reset Timing

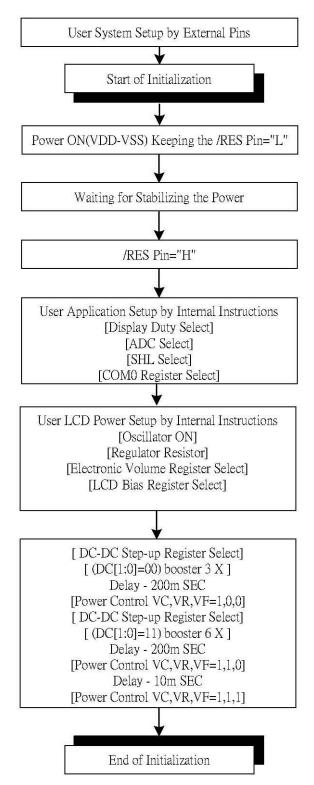


(VDD = 3.3V , Ta = –30 to 85° C)

Item	Signal	Symbol	Condition		Units		
			Condition	Min.	Тур.	Max.	Units
Reset time		tR		_	_	1	us
Reset "L" pulse width	RESB	tRW		1	_	_	us

12.2.3 . Initializing by Instruction

Referential Instruction Setup Flow: Initializing with the built-in Power Supply Circuits

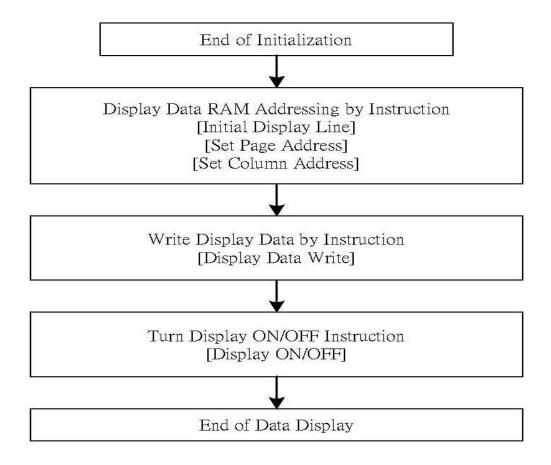


IN

BOLYMIN



Referential Instruction Setup Flow: Data Displaying



Referential Instruction Setup Flow: Power OFF

